

ABSTRACT

A plasma display panel is provided which is free from blisters and pinholes on its dielectric layers and has excellent characteristics of breakdown voltage. The plasma display panel has a multilayered first
5 dielectric layer covering a display electrode including a scanning electrode and a sustain electrode that are provided on a front substrate, and a multilayered second dielectric layer covering a data electrode that is provided on a back substrate. The periphery of an upper dielectric layer of the first dielectric layer and/or the second dielectric layer is positioned
10 identically or partially in size and shape to a periphery of a lower dielectric layer to be formed.